



AD2100 BLOCK DIAGRAM

SPECIFICATIONS

Analog Inputs	(2) singled-ended into 50 ohms SMA
A/D Resolution	14-bit (1 part in 16384)
Conversion Rate	100 MS/s maximum, 1 MS/s min.
Sampling Method	Synchronous
Acquisition mode	One Shot
Input Bandwidth	50 MHz minimum
Input Ranges	
Fixed Gain	+/- 1.0 Volts
Fixed Gain Chg	User Specified (+/- 0.25V to +/-2V)
Input Coupling	DC
Input Offset	+/- 5.0 Volts Maximum
Maximum Input Voltage	+/- 6 V
Memory Size (total)	1048576 samples maximum
Segment Sizes	Software selectable segment sizes of 128, 256, 512, ... 1048576 (1M). 1024 Segments or 1M memory (whichever is lower)
Segment Number	
Timebase	
Standard	Software selectable at 100 MHz, 50 MHz, 25 Mhz, ... , 1 MHz.
External Clk	1 MHz <= ExtClk <= 100 MHz TTL
Trigger	
Location	Start of Segment
Sources	External TTL, Software
Slope	Positive or Negative
Coupling	DC, TTL levels

GENERAL

Power Supply	+5V @ 860mA (6.25 MSPS) +5V @ 1240mA (100 MSPS)
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Operating Temperature	-40 to +85 degrees C Standard
Operating Humidity	5% to 95% noncondensing
Size	Standard PC/104-Plus Card
Connectors	SMA

Option Summary:

Option 1:	4 Megasamples
Option 2:	100 MHz Input Bandwidth
Option 3:	Fixed Gain Change Request
Option 4:	Linux Drivers

<i>Model Number</i>	<i>Description</i>
AD2100-14-1M-104P	Basic 2-Ch, 100 MSPS w/1MEG Memory
Option 1	4 MEG Memory
Option 2	100 MHz BW
Option 3	Fixed Gain Change Req.
Option 4	Linux Drivers

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